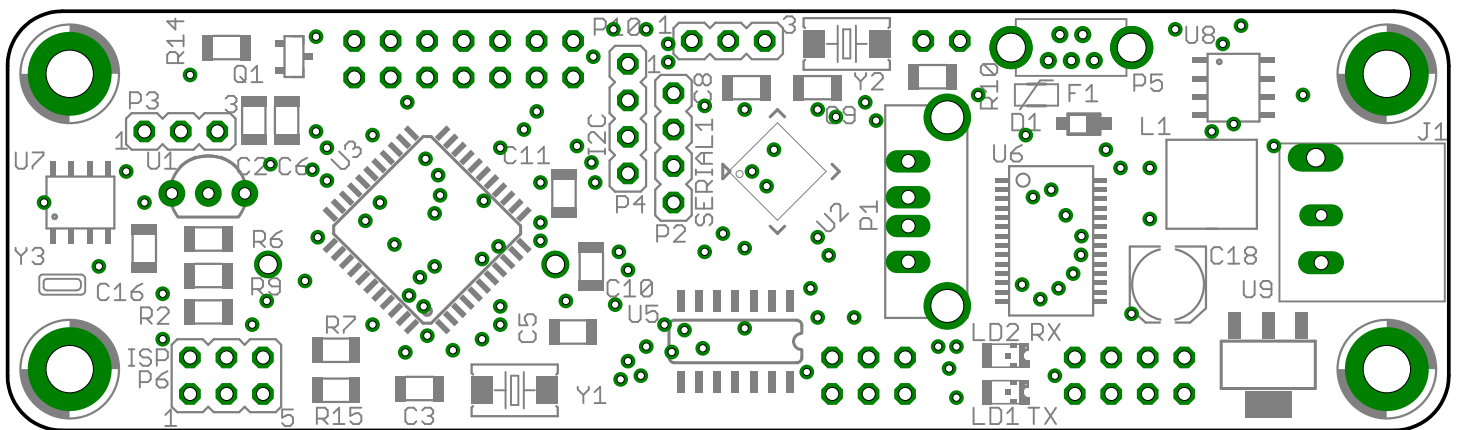


Pict 1. FluoroDuino top side component layout



Pict 2. FluoroDuino bottom side component layout