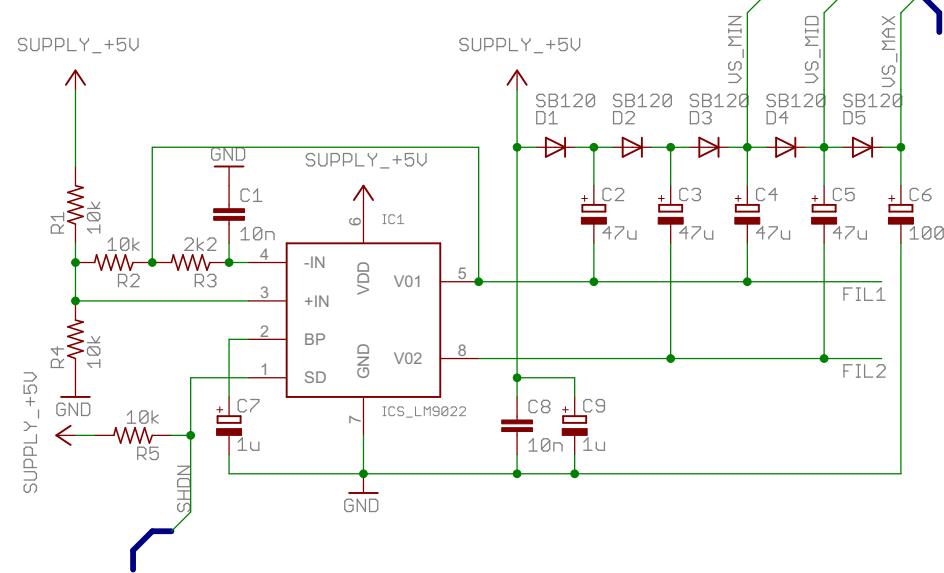
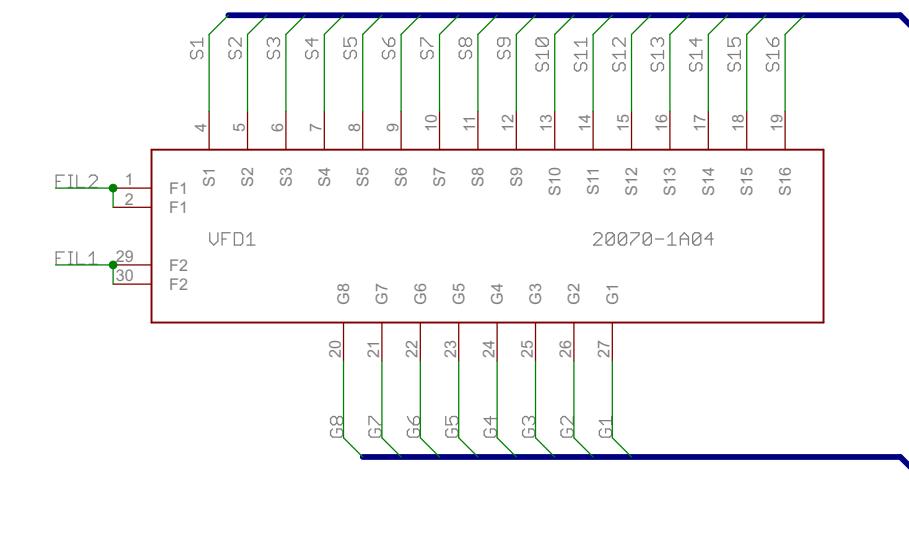


1 2 3 4 5 6 7 8

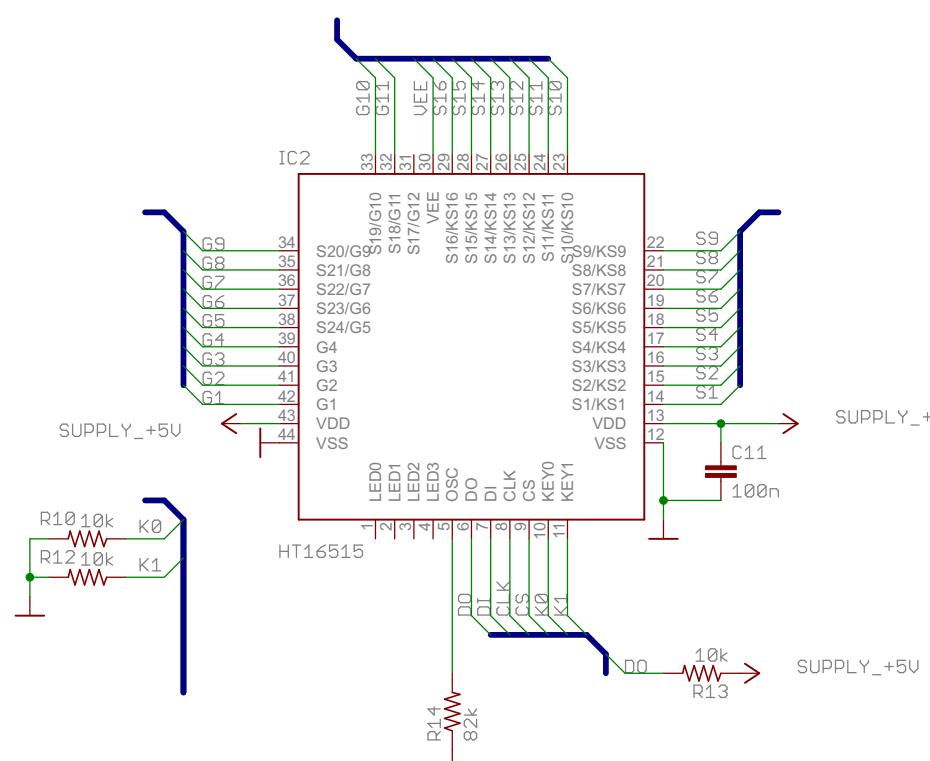
HV & Filament PSU



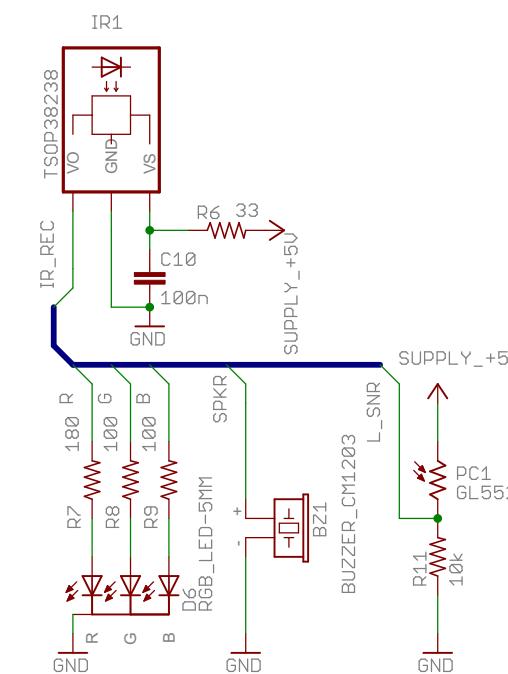
VFD (16 seg./8 digits)



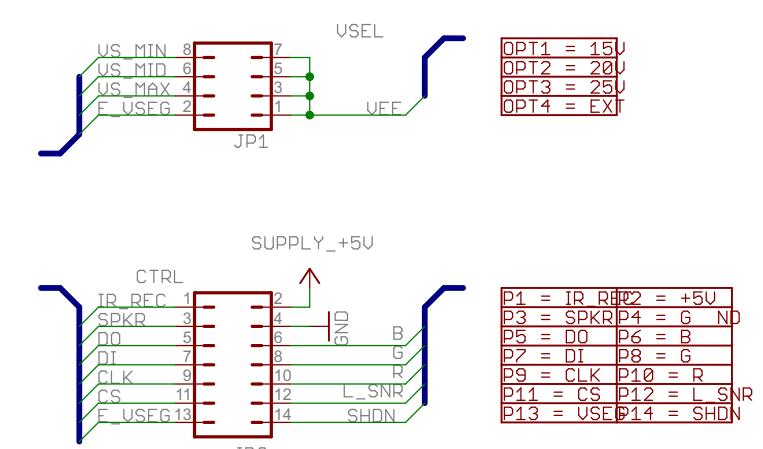
Driver/Controller



Peripheral



Interfaces



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 Sheet: 1/1	
Majictale http://atmega.majictale.com	MUFDPPanel_16S8D
Majictale VFDpanel 16 Segments x 8 Digits	8/16/2013 8:24:00 PM
drawn by Dmitry Pakhomenko	

1 2 3 4 5 6 7 8